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1 METHOD AND SYSTEM FOR ELECTRONIC CIRCUIT DESIGN

2 Abstract of the Disclosure

3 A method for compressing an integrated circuit model has steps of  
4 selecting a first net and compressing at least a second net connected to the first  
5 net by removing resistors and summing capacitors. Through steps of the  
6 present invention, the size and complexity of the integrated circuit model are  
7 thereby simplified while retaining information regarding the second net that  
8 may be required for analysis.

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